

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICERECEIVED  
CENTRAL FAX CENTER  
JUN 13 2005

APPLICANTS: Nicholas J. Kelsey, et al.  
SERIAL NO.: 09/748,098  
FILING DATE: December 21, 2000  
TITLE: System and Method for Instruction Level Multithreading in an Embedded Processor Using Zero-Time Context Switching  
EXAMINER: Charles A. Harkness  
GROUP ART UNIT: 2183  
ATTY. DKT. NO.: 20880-05093

## CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence, including the enclosures identified above, is being transmitted on the date shown below via facsimile to: Commissioner for Patents at the facsimile number indicated below.

Signature:

Typed or Printed Name: Hector J. Ribera, Reg. No. 54,397

Dated: June 13, 2005

Facsimile Number: 1-703-872-9306

MAIL STOP AF  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

AMENDMENT D AND RESPONSE UNDER 37 C.F.R. §1.116

Sir:

This amendment for the patent application identified above is in response to the Office Action dated April 13, 2005, which set a shortened statutory period for response that expires on July 13, 2005.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 12 of this paper.

Kindly amend this application as indicated herein.

-- Do Not Enter --  
6-15-05  
HLS